

**Amendments to the Claims:** This listing of claims will replace all prior versions, and listings, of claims in the application

Listing of Claims:

1. (Canceled)
2. (Previously Presented) An image capture system, comprising:
  - a plurality of rows of pixels, each row comprising:
    - a reset line for providing a reset signal;
    - a plurality of pixels, each pixel comprising:
      - a first FET having a gate terminal coupled to a reset line, a drain terminal coupled to a supply voltage, and a source terminal coupled to a readout node; and
      - a photodetector coupled between a first ground and the readout node;
      - a switching device selectively coupled to one of reset lines in the rows of pixels;
      - a reference voltage source coupled between a second ground and one of the reset lines via the switching device, wherein the reference voltage source generates a reset voltage that is independent of the supply voltage and the first and second grounds have the same potential; and
      - an operational amplifier buffer comprising (1) an output coupled by the switching device to one of the reset lines, (2) a non-inverting input coupled to the reference voltage source to receive the reset voltage, and (3) an inverting input coupled to the output in a feedback loop, wherein the feedback loop does not pass through the readout node.
  3. (Currently Amended) The image capture system of claim-1\_2, wherein the first FET further comprises an n-channel enhancement mode MOSFET.
  4. (Currently Amended) The image capture system of claim-1\_2, wherein the ground referenced reset voltage is greater than the supply voltage.
  5. (Currently Amended) The image capture system of claim-1\_2, further comprising a second FET having a gate terminal coupled to the readout node and a drain terminal coupled to the supply voltage.

6. (Previously Presented) The image capture system of claim 5, further comprising a third FET having a gate terminal coupled to a row select line, a source terminal coupled to a column line, and a drain terminal coupled to a source terminal of the second FET.

7-13 (Canceled)

14. (Previously Presented) The image capture system of claim 6, wherein the switching device comprises a multiplexer.

15-21.(Canceled)